### DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that my residence, post office address and cruzenship are as stated below next to my name, and
I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plimal names are
listed below) of the subject matter which is claimed and for which a patent is sought on the invention cutiled

Semiconductor storage device and refresh control method thereof the specification of which (check one)

•		•	•				
	$\mathcal{A}$			nted States Application h (nf applicable)	Number or PCT Intern	ational Applicati	on Number <u>PCT/JP02/13555</u>
any an Code applia certifi	mendment of Federal cation(s) for icate disciplination (s)	referred to abo Regulation, § r patent or my sing the subject	ve I acknowledge the 156 I hereby claim for entor's certificate listed at matter claimed in the	comients of the above-to duty to disclose informa xeign priority benefits in I below and have also xi- ar application and having fore the filing date of this	ation which is material inder Title 35, United t entified below any for g a filing date (1) before	to patentability States Code, §119 sign application:	as defined in Title 37, 9/365 of any foreign for patent or inventor's
				Prior Foreign Applica	ation(s)		
И	umber		Country	Day/Month/Year File	<u>ed Proor</u>	ty Claimed	
20	01-3975	575	JAPAN	27/12/2001	🖔 т	es () No	
					() Y	es () No	
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Interr the pr duty	national Ap nor United to disclose	plications liste States applicat information w	d above or below, and non in the manner prov hich is material to pate	es Code, § 120/365 of any nisofar as the subject monded by the first paragra mability as defined in Titon and the national or Po	atter of each of the cli ph of Title 35, United tile 37, Code of Federi	ums of this appli States Code, §11 I Regulations, §1	cation is not disclosed in 12, I acknowledge the 1 56 which became
(App	lication Nu	mber)	Day/N	Month/Year Filed	Status (Patent	d, Pending, Aba	ndoned)
Herse 45,69 No 4	chbach Jarr 98, C Hunt 18,946, and	ell, Reg No 3 er Baker, Reg Nadège M La	9,223, Kevin Tormey, No 46,533, Monica C	978, Anne E Saturnelli, I Reg No 41,351, Elizab Gerber, Reg No 46,724, 18 as attorneys and/or ago	eth E Nugent, Reg 1 Elyah Cocks, Reg 10	o 43,839, Valar o 47,499, John A	ie B Rosen, Reg No A Hamilton, III, Reg
Addı	ess all telep	phone calls to I	Donald W Murhead a	at telephone number (617 Patent Group Chuate, Hall & Stew Exchange Place 53 State Street Boston, Massachuse	æri	all corresponden	ce to
belie puni: may	wed to be to shable by fi jeopardize	rue, and further ine or imprisor the validity of	r that these statements iment, or both, under S the application or any	y own knowledge are tro were made with the knowlection 1001 of Trile 18 of patent issued thereon	wledge that willful fal of the United States Co	se statements and	i the like so made are willful false statements
		nre /qua	7	ly name) <u>Hirovuk</u>			
	.K.	anagawa, J	apan	ly name)	Citizens	Japanese	<del></del>
	dence		c/o NEC	Electronics Corpor			akahara-ku,
Posi	Omce And	iress (melude 2	ap wie)	shi. Kanagawa 211			<del></del>
Inve	ntor's signa	ature	akuya Idirot		· •	ne 24,	2004
		le or first mve	ntor (given name, fami	ly name) Takuya I	IIROTA		
Resi	dence Kar	nagawa, Jaj	pan W.A.		Citizensi		
	· <del></del> -	dress (mclude :	c/o NE(	C Electronics Corpo	oration, 1753, Shi	monumabe, l	Nakahara-ku,
		•	•	shi, Kanagawa 211	-8668, Japan		

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() Additional inventors are being named on separately numbered sheets attached hereto.

# DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION (continued)

`	Inventor's signature Royuaki 7	V mother!		Date J	une 24	20	n4					
h		Morri	aki KOMAI		une 24	,_ <u>=</u> v						
K	Full name of sole or first inventor (given name, f Kanagawa, Japan	amiy name) T			Jap	anese	_					
`)	Kesidence	EC Electronics C	orporation, 1	Catzenship 753, Shima	·							
	Post Office Address (melude zip code)											
$l_i$	Inventor's signature Atsushi 7	nakagawa			une 24	, 20	04					
10	Full name of sole or first inventor (green name, family name) Atsushi NAKAGAWA											
W	Residence Kanagowa, Japan	Crizenship Japanese										
	Post Office Address (maked zip code) c/o N	VEC Electronics	Corporation,	1753, Shin	ionumabe,	Nakah	ara-ku					
	Kawasaki-shi, Kanagawa 211-8668, Japan											
7	Inventor's aignature <u>Susumu</u>	Takano	<b>3</b>	Date	June 2	4, 2	2004					
M	Full name of sole or first inventor (given name, i	family name) _Sus	umu TAKA	<del></del>								
$\stackrel{\sim}{\sim}$	Residence Kanagawa Japan	<b>-</b>		Crtizenshij	Japa	anese						
)	Post Office Address (mclude zrp code)	EC Electronics C	orporation, 1	753, Simor	umabe, Na	kabar	a-ku _					
	Kawa	saki-shi, Kanag	awa 211-8668	, Japan								
7	Inventor's signature Mossahin	•	<u> </u>	Date	June 2	4. 2	2004					
B	Full name of sole or first inventor (given name,	family name) <u>Ma</u>	sahiro YOS	HIDA	<del></del>							
Ò,	Residence Kanagawa, Japan	<u>+</u>		Crtizenshi	·	anese						
<u>S</u>	Post Office Address (include zip code)c/o l	VEC Electronics	Corporation,	1753, Sime	onumabe, N	Vakaha	ra-ku					
	Kaw	vasaki-shi, Kana	gawa 211-866	8, Japan	ı							
1.	Inventor's signature	Torige	(堂)	Date	June 2	4, 2	2004_					
$\mathcal{P}$	Full name of sole or first inventor (given name,	37	uji TORIGE	}								
N	Residence Kanagawa, Japan	T.		Crtizenshi	Jap	anese	<u> </u>					
\	Post Office Address (include zip code)	NEC Electronics	Corporation,	1753, Sim	onumabe, N	Vakaha	ra-ku					
$G_{i}$	inventor's signature	vasaki-shi, Kana Laba	第211-866	88, Japan Date	June	24,	2004					
$\mathcal{C}_{\mathcal{I}}$	Full name of sole or first inventor (given name,	family name) Hi	leo INABA	· —————								
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•		NEC Micro Syste	ms, Ltd., 403	-53, Kosug	imachi, 1-c	home,						
	Nakahara-ku, Kawasaki-shi, Kanagawa Japan											

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# DT04 Rec'd PCT/PT0 2 8 JUN 2004

**PATENT** 

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hiroyuki TAKAHASHI, et al.

Appl. No.: To be assigned

Art Unit: To be assigned

Filed: June 28, 2004

Examiner: To be assigned

For: **SEMICONDUCTOR STORAGE** 

SEMICONDUCTOR STORAGE

DEVICE AND REFRESH

**CONTROL METHOD THEREOF** 

Atty. Docket: UPM-02501

#### **Certificate of Express Mailing**

I hereby certify that the foregoing document is being deposited with the United States Postal Service as Express Mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 28, 2004.

Tracey A. Newell

Express Mail Label No.: EV 325286758 US

### **STATEMENT UNDER 37 C.F.R.§ 1.32(c)(3)**

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sirs:

Pursuant to 37 C.F.R. § 1.32(c)(3), Applicant hereby selects the following ten practitioners to be made of record for the above-referenced patent application:

Donald W. Muirhead, Reg. No. 33,978;

Anne E. Saturnelli, Reg. No. 41,290;

Sam Pasternack, Reg. No. 29,576:

Brenda Herschbach Jarrell, Reg. No. 39,223;

Elizabeth E. Nugent, Reg. No. 43,839;

Valarie B. Rosen, Reg. No. 45,698;

C. Hunter Baker, Reg. No. 46,533;

Monica Gerber, Reg. No. 46,724:

Elijah Cocks, Reg. No. 47,499; and

Stacy Blasberg, Reg. No. 52,625



## DT04 Rec'd PCT/PTO 28 JUN 2004

Although we believe that we have appropriately provided for any fees due in connection with this submission, the Commissioner is authorized to credit any overpayment or charge any deficiencies to/from our **Deposit Account No. 031721**. Two originally-executed copies of this form are being submitted.

Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at (617) 248-4038.

June 28, 2004

Date

Patent Group

Choate, Hall & Stewart

Exchange Place

53 State Street

Boston, MA 02109-2804

Respectfully submitted,

CHOATE, HALL & STEWAR

Donald W. Muirhead

Reg. No. 33,978